

| | Type | Hits | Search Text | DBs |
|----|------|--------|---|-------|
| 1 | BRS | 1508 | zero adj generat\$5 | USPAT |
| 2 | BRS | 153 | zero adj generator | USPAT |
| 3 | BRS | 153 | "zero generator" | USPAT |
| 4 | BRS | 1 | virtual adj zero adj register | USPAT |
| 5 | BRS | 0 | zero adj register adj renamer | USPAT |
| 6 | BRS | 0 | zero adj register adj renam\$3 | USPAT |
| 7 | BRS | 608001 | zero register | USPAT |
| 8 | BRS | 822 | zero adj register | USPAT |
| 9 | BRS | 0 | (delet or remov\$3) adj zero\$3 adj instruction | USPAT |
| 10 | BRS | 0 | (delet\$3 or remov\$3) adj zero\$3 adj instruction | USPAT |
| 11 | BRS | 6 | (delet\$3 or remov\$3) near2 zero\$3 near2 instruction | USPAT |
| 12 | BRS | 479 | ("zero register") and instruction | USPAT |
| 13 | BRS | 418 | ("zero register") and instruction and processor | USPAT |
| 14 | BRS | 572 | zero\$3 adj instruction | USPAT |
| 15 | BRS | 3 | (delet\$3 or remov\$3 or discard\$3 or trash\$3 or throw\$3 or eliminat\$3) near2 (zero\$3 adj instruction) | USPAT |

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|----|------|------|---|-------|
| 16 | BRS | 4 | (delet\$3 or remov\$3 or discard\$3 or trash\$3 or throw\$3 or eliminat\$3) near2 (zero\$3 near2 instruction) | USPAT |
| 17 | BRS | 0 | renam\$3 near2 (zero adj register) | USPAT |
| 18 | BRS | 0 | renam\$3)near2(z ero adj register | USPAT |
| 19 | BRS | 0 | renam\$3)near2(z ero adj register | USPAT |
| 20 | BRS | 0 | renam\$3)near2(z ero adj register | USPAT |
| 21 | BRS | 1 | implement\$3 near2 (zero adj register) | USPAT |
| 22 | BRS | 11 | (zero\$3 adj register) and (zero\$3 adj instruction) | USPAT |
| 23 | BRS | 1 | emulat\$3)near2(z ero\$3 adj register | USPAT |
| 24 | BRS | 0 | 712/2266 | USPAT |
| 25 | BRS | 226 | 712/226.ccls. | USPAT |
| 26 | BRS | 373 | 712/217.ccls. | USPAT |
| 27 | BRS | 350 | 712/227.ccls. | USPAT |
| 28 | BRS | 4 | 712/226.ccls. and (zero adj register) | USPAT |
| 29 | BRS | 3 | eliminat\$3 near2 (zero adj instruction) | USPAT |
| 30 | BRS | 0 | eliminat\$3 near2 (zero adj register) | USPAT |
| 31 | BRS | 3 | eliminat\$3 near3 (zero adj register) | USPAT |

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|----|------|------|--|---|
| 32 | BRS | 0 | (zero adj register) near2 absent | USPAT |
| 33 | BRS | 0 | renam\$3 same (zero adj register) | USPAT |
| 34 | BRS | 0 | renam\$3 same (zero adj register) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 35 | BRS | 3 | eliminat\$3 near3 (zero adj register) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 36 | BRS | 1 | emulat\$3 near2(zero\$3 adj register) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 37 | BRS | 0 | (delet or remov\$3) adj zero\$3 adj instruction | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 38 | BRS | 9 | (delet\$3 or remov\$3) near2 zero\$3 near2 instruction | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 39 | BRS | 0 | constant near2 (register adj renam\$3) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 40 | BRS | 0 | constant near3 (register adj renam\$3) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 41 | BRS | 0 | constant near4 (register adj renam\$3) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 42 | BRS | 21 | constant near4 (register near2 renam\$3) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 43 | BRS | 762 | zero adj instruction | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 44 | BRS | 4 | (zero adj instruction) and hardware near2 optimiz\$3 | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |

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|----|------|------|---|--|
| 45 | BRS | 3146 | intel.as. and zero | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 46 | BRS | 7 | eliminate with apparent with dependency | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 47 | BRS | 36 | ((software or code)near2 optimiz\$5) and ("xor" same (zero\$3 or clear\$3)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 48 | BRS | 4655 | ((software or code)near2 optimiz\$5) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 49 | BRS | 0 | ((software or code)near2 optimiz\$5)) same (logical adj zero) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 50 | BRS | 491 | (delet or remov\$3 or rid) adj instruction | USPAT |
| 51 | BRS | 809 | (delet\$3 or remov\$3 or rid) adj instruction | USPAT |
| 52 | BRS | 8 | "xor" adj r0 | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 53 | BRS | 41 | "xor" adj r1 | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 54 | BRS | 357 | (watch\$3 or locat\$3 or detect\$) near2 ("xor") | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 55 | BRS | 132 | (watch\$3 or locat\$3 or detect\$) adj ("xor") | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 56 | BRS | 37 | axax | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 57 | BRS | 11 | "xor" adj axax | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |

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|----|------|-------|--|---|
| 58 | BRS | 15067 | (watch\$3 or locat\$3 or detect\$) adj (zero\$3 or clear\$3) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 59 | BRS | 11974 | (watch\$3 or locat\$3 or detect\$) adj (zero\$3) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 60 | BRS | 34 | (watch\$3) adj (zero\$3) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 61 | BRS | 9651 | (detect\$) adj (zero\$3) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 62 | BRS | 73 | ((detect\$) adj (zero\$3)) with instruction | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 63 | BRS | 11 | (monitor\$3) adj ("xor") | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 64 | BRS | 20 | zero adj out adj register | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 65 | BRS | 2 | zero adj out adj instruction | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 66 | BRS | 19 | zeroing adj instruction | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 67 | BRS | 315 | map\$ adj zero | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 68 | BRS | 614 | (delet or remov\$3 or rid or discard) adj instruction | USPAT |
| 69 | BRS | 15736 | (watch\$3 or locat\$3 or detect\$ or monitor\$3) adj (zero\$3 or clear\$3) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |

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|----|------|------|--|--|
| 70 | BRS | 13 | ((delet or remov\$3 or rid or discard) adj instruction) and ((watch\$3 or locat\$3 or detect\$ or monitor\$3) adj (zero\$3 or clear\$3)) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 71 | BRS | 289 | map\$3 adj zero | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 72 | BRS | 0 | zeroed adj out adj register | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 73 | BRS | 0 | zerod adj out adj register | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 74 | BRS | 27 | zeroed adj register | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 75 | BRS | 506 | (watch\$3 or locat\$3 or detect\$) adj (zero\$3 or clear\$3) same instruction | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 76 | BRS | 1 | (watch\$3 or locat\$3 or detect\$) adj ((zero\$3 or clear\$3) with instruction) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 77 | BRS | 3 | (watch\$3 or locat\$3 or detect\$ or check\$3) adj ((zero\$3 or clear\$3) with instruction) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 78 | BRS | 9 | (zero\$4 adj instruction) and (eliminat\$3 near2 dependency) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |

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|----|------|------|--|--|
| 79 | BRS | 106 | Samra.in. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 80 | BRS | 19 | Samra.in. and nicholas.in. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 81 | BRS | 4655 | ((software or code)near2 optimiz\$5) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 82 | BRS | 158 | ((software or code)near2 optimiz\$5) and (instruction near2 translation) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 83 | BRS | 24 | ((software or code)near2 optimiz\$5) and (instruction near2 translation) and ((eliminat\$3 or, delet\$3 or discard\$3 or remov\$3)adj instruction) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 84 | BRS | 2 | 6038652.pn. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 85 | BRS | 1 | ((replac\$3) adj register) near3 immediate | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 86 | BRS | 6 | ((replac\$3) adj register) near3 (immediate or zero) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 87 | BRS | 465 | (zero\$3 adj instruction) and processor and instruction | USPAT |
| 88 | BRS | 221 | (zero\$3 adj instruction) and processor and instruction and (optimiz\$3 or compress\$3) | USPAT |
| 89 | BRS | 22 | "xor" adj zero | USPAT |

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|----|------|------|---|-------|
| 90 | BRS | 1 | 3905025.pn. | USPAT |
| 91 | BRS | 0 | (eliminat\$3) near2 "xor" near2 instruction | USPAT |
| 92 | BRS | 0 | (eliminat\$3 or delet\$3 or remov\$3 or discard\$3 or rid) near2 "xor" near2 instruction | USPAT |
| 93 | BRS | 55 | (eliminat\$3 or delet\$3 or remov\$3 or discard\$3 or rid) near2 "xor" | USPAT |
| 94 | BRS | 23 | (eliminat\$3 or delet\$3 or remov\$3 or discard\$3 or rid) adj "xor" | USPAT |
| 95 | BRS | 87 | translat\$4 same (optimiz\$3 adj (code or instruction or software)) | USPAT |
| 96 | BRS | 0 | translat\$4 same ((optimiz\$3 adj (code or instruction or software)) with ("xor" or zero\$3 or clear\$3)) | USPAT |
| 97 | BRS | 0 | translat\$4 same (((optimiz\$3 or reduc\$3 or compress\$3) adj (code or instruction or software)) with ("xor" or zero\$3 or clear\$3)) | USPAT |

| | Type | Hits | Search Text | DBs |
|-----|------|------|---|-------|
| 98 | BRS | 0 | translat\$4 same (((optimiz\$3 or reduc\$3 or compress\$3 or delet\$3) adj (code or instruction or software)) with ("xor" or zero\$3 or clear\$3)) | USPAT |
| 99 | BRS | 29 | translat\$4 and (((optimiz\$3 or reduc\$3 or compress\$3 or delet\$3) adj (code or instruction or software)) with ("xor" or zero\$3 or clear\$3)) | USPAT |
| 100 | BRS | 2479 | (delet\$3 or remov\$3 or discard\$3 or trash\$3 or throw\$3 or eliminat\$3) near2 (instruction) | USPAT |
| 101 | BRS | 15 | (delet\$3 or remov\$3 or discard\$3 or trash\$3 or throw\$3 or eliminat\$3) near2 (instruction) sa me ((register adj alias adj table) or (renam\$3 adj register) or (register adj translat\$3)) | USPAT |

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|-----|------|------|--|-------|
| 102 | BRS | 16 | (delet\$3 or remov\$3 or discard\$3 or trash\$3 or throw\$3 or eliminat\$3 or merg\$3 or combin\$3) near2 (instruction) same ((register adj alias adj table) or (renam\$3 adj register) or (register adj translat\$3)) | USPAT |
| 103 | BRS | 11 | intel.as. and (floating adj point) and merg\$3 near2 instruction | USPAT |
| 104 | BRS | 12 | intel.as. and (floating adj point) and merg\$3 near2 instruction | USPAT |
| 105 | BRS | 34 | intel.as. and (floating adj point) and swap | USPAT |
| 106 | BRS | 2438 | (eliminat\$3 or delet\$3 or remov\$3 or discard\$3 or rid) near2 instruction | USPAT |
| 107 | BRS | 1271 | (delet\$3 or remov\$3 or discard\$3 or trash\$3 or throw\$3 or eliminat\$3 or merg\$ or combin\$33) near2 (instruction) and (optimiz\$3 or compress) | USPAT |
| 108 | BRS | 0 | intel.as. and (floating adj point adj swap) and (merg\$3 or combin\$3) adj two adj instruction | USPAT |

| | Type | Hits | Search Text | DBs |
|-----|------|------|---|-------|
| 109 | BRS | 0 | intel.as. and (floating adj point adj swap) and (merg\$3 or combin\$3) adj instruction | USPAT |
| 110 | BRS | 0 | intel.as. and (floating adj point with swap) and (merg\$3 or combin\$3) adj instruction | USPAT |
| 111 | BRS | 28 | intel.as. and (merg\$3 or combin\$3) adj instruction | USPAT |
| 112 | BRS | 12 | intel.as. and merg\$3.ti. | USPAT |
| 113 | BRS | 1 | intel.as. and merg\$3.ti. and exchange | USPAT |
| 114 | BRS | 23 | intel.as. and RAT and merg\$3 | USPAT |
| 115 | BRS | 9 | intel.as. and RAT and merg\$3 and swap and exchange | USPAT |
| 116 | BRS | 83 | trace adj cache | USPAT |
| 117 | BRS | 1 | trace adj cache adj line | USPAT |
| 118 | BRS | 3 | (trace adj cache).ti. | USPAT |
| 119 | BRS | 16 | (register adj alias adj table) and exchange | USPAT |
| 120 | BRS | 410 | (compile adj time) with (run adj time) | USPAT |
| 121 | BRS | 3947 | r31 aplha | USPAT |
| 122 | BRS | 0 | r31 and aplha | USPAT |
| 123 | BRS | 1075 | zero adj register | USPAT |
| 124 | BRS | 363 | r31 and alpha | USPAT |
| 125 | BRS | 98 | r31 and alpha and processor | USPAT |

| | Type | Hits | Search Text | DBs |
|-----|------|------|--|-------|
| 126 | BRS | 0 | (r31 and alpha and processor) and (reads adj as adj zero) | USPAT |
| 127 | BRS | 2 | (r31 and alpha and processor) and (read adj zero) | USPAT |